

Abstract: We present a new educational platform for teaching reconfigurable logic. The new platform includes two FPGAs, separate instruction and data buses, serial and parallel port connections, and extensive I/O connections for interfacing with other reconfigurable computing boards. The platform was successfully tested at the University of New Mexico, the University of Texas at Austin, the University of Texas at El Paso, and the West Point military academy. Online documentation details the entire design, fabricating, assembly, and testing phases. All design information is freely available online. © 2009 Wiley Periodicals, Inc. *Comput Appl Eng Educ* 17: 232-239, 2009; Published online in Wiley InterScience (www.interscience.wiley.com); DOI 10.1002/cae20192